Appl. No. 10/708,103 Amdt. dated August 22, 2006 Reply to Office action of June 23, 2006

Abstract

An address decoding method and related apparatus for deciding which section of a memory device a given address belongs. The memory device has a plurality of sections, each section has a plurality of memory units, and each memory unit has a unique address. The method includes: comparing some specific bits of the given address with predetermined values for deciding which section the given address belongs.

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